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J6714 U.S. PTO

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

J6714

APPL NUM 10067424	FILING DATE 02/07/2002	CLASS 257	SUBCLASS 1003	GAU 2815	EXAMINER <i>Brock</i>
**APPLICANTS: Lu Jeng; Mei Ping; Lemmi Francesco; Street Robert; Boyce James;					
<i>BEST AVAILABLE</i>					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/419,293 10/15/1999					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 7447.0021-01	
TITLE : Dual dielectric structure for suppressing lateral leakage current in high fill factor arrays U.S.DEP'T. OF COMM./PAT. & TM-PTO-43SL(Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Total Claims	Print Claim for O.G	
<input type="checkbox"/> TERMINAL		Sheets Drwg. Figs. Drwg. Print Fig.		
DISCLAIMER				
PREPARED FOR ISSUE		Application Examiner		
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